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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Adiletta et al.

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Examiner: Ellis, Richard L

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Assignee: Intel Corporation

Title

: PARALLEL PROCESSOR ARCHITECTURE

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION MAILED 3-14-06

Claim Listing/Amendments

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Remarks

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